ABSTRACT OF THE DISCLOSURE

An object of the present invention is to provide a semiconductor device capable of adapting to an increase in the external terminals which can be arranged on the mount surface (a greater number of pins). A mesa-type semiconductor chip is mounted on a mount surface of a substrate which is a semiconductor chip carrying portion such that the side wall surface of the four side walls of the first semiconductor chip intersects the mount surface at an acute angle θ (0°<0<90°). Further, a first pad formed on a main surface of the first semiconductor chip is electrically connected to a solder ball provided on an unmounted surface, via a first wiring layer, one end of which is connected to the first pad, and which extends along the main surface, the side wall surface, and the unmounted surface of the semiconductor chip.